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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MATTHIAS BONKAß
AXEL PREUSSE
MARKUS NOPPER

Examiner: Luan V. Van

Group Art Unit: 1753

Serial No.: 10/629,106

Att'y Docket: 2000.108300/DE0340

Filed: July 29, 2003

Customer No.: 23720

For: METHOD OF PLATING A
SEMICONDUCTOR STRUCTURE

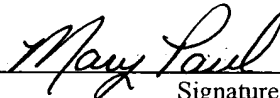
**RESPONSE TO RESTRICTION
REQUIREMENT DATED JULY 25, 2005**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, this 2nd day of August, 2005.


Signature

This paper is submitted in response to the Restriction Requirement dated July 25, 2005, for which the one-month date for response is August 25, 2005.

In response to the restriction requirement which the Examiner imposed, Applicants elect, with traverse, to prosecute claims 1-46, *i.e.*, the Group I claims.

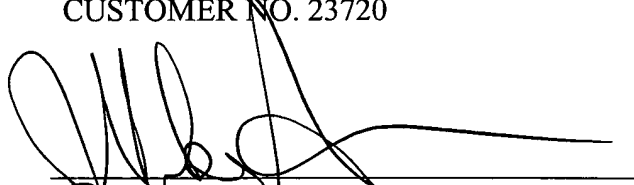
It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Director is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0340. In the event the monies in that account are insufficient, the Director is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.108300.

The Examiner is invited to contact the undersigned attorney at (713) 934-4055 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON
CUSTOMER NO. 23720

Date: August 2, 2005



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